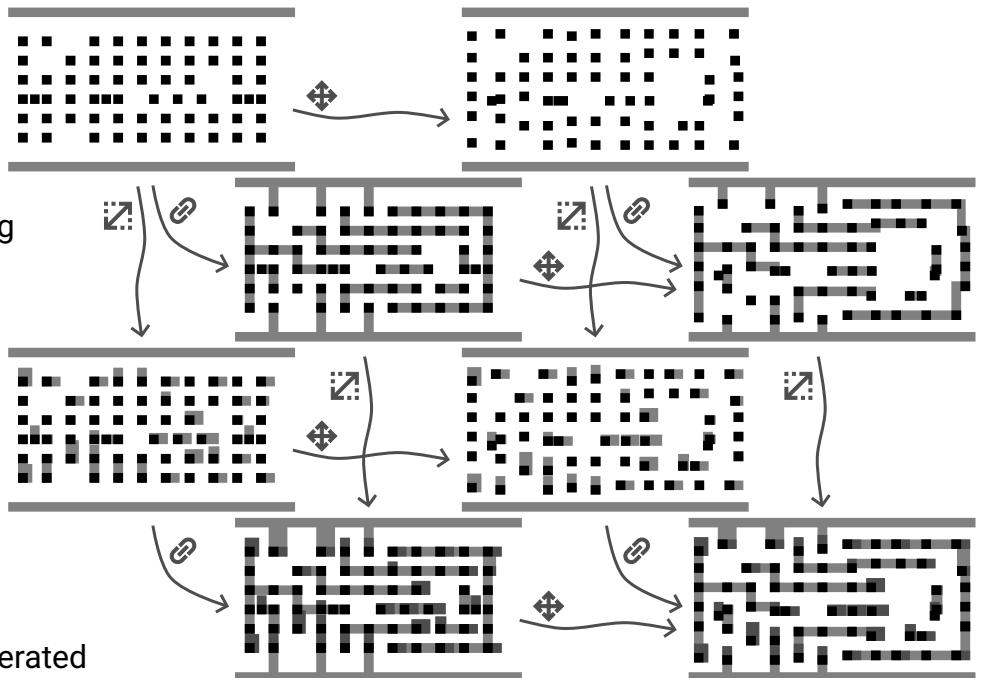
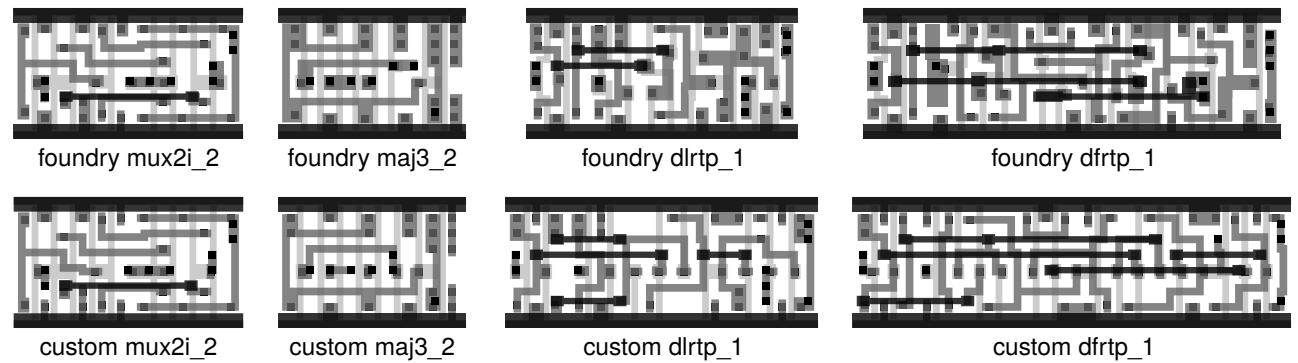


(a) steps of generating a standard cell layer



(b) example cells generated vs their foundry counterparts



(c) structure of digital design to test the custom cells

